



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application

Inventor(s): Stephen W Melvin  
Application No.: 10/065,340  
Confirm. No.: 2218  
Filed: 10-06-2002  
Title: HARDWARE ENFORCED  
VIRTUAL SEQUENTIALITY

PATENT APPLICATION

Art Unit: 2186  
Examiner: ELMORE,  
STEPHEN C

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed are replacement drawings addressing the Examiner's comment in the Notice of Allowability mailed on September 13, 2005. The shading has been removed from Fig. 4 and the cross hatching has been removed from Fig. 6.

Also enclosed are a signed PTOL-85 Part B and a PTO-2038 for Credit Card payment of the Issue Fee.

Respectfully submitted,

Date: September 19, 2005

By:   
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Reg. No. 50,467

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